Pin	TX6LVDS	TX6RGB	TXUL	TX8M	TX8P	TXMP		
1							POWER-SUPPLY	
2							VIN	
3								
4								
5							VOUT	
6								
7								
8							BOOTMODE	
9							VOUT	
10								
11								
12							RESET	
13							VBACKUP	
14								
15	u	u		d	d	I	#RESET_OUT	
16							#POR	
17							#RESET_IN	
18							ETHERNET	
19							ETN_TXN	
20							#ETN_LINKLED	
21							ETN_TXP	
22							ETN_3V3	
23							ETN_RXN	
24							#ETN_ACTLED	
25							ETN_RXP	
26							USB #1	
27							USBH_VBUSEN	
28							#USBH_OC	
29							USBH_DM	
30							USBH_VBUS	
31							USBH_DP	
32							USB #2	
33							USBOTG_ID	
34							USBOTG_VBUSEN / TXCAN	
35							USBOTG_DM	
36							#USBOTG_OC / RXCAN	
37							USBOTG_DP	
38							USBOTG_VBUS	
39							I2C/PWM/SPI	
40	u	u	A	d	d	I	I2C_SDA	
41	u	u	A	d	d	I	I2C_CLK	
42	u	u	k	D	D	I	PWM	
43	u	u		d	d			
44	u	u	k	d	d	I	SPI_SS0	
45	u	u	u	d	d		SPI_SS1	
46	u	u	k	d	d	I	SPI_MOSI	
47	u	u	k	d	d	I	SPI_MISO	
48	u	u	k	d	d	I	SPI_SCLK	
49	u	u			d		SPI_RDY	
50							SD-CARD #1	
51	u	u	k	d	d	I	SD1_CD	
52	u	u	k	d	d	1)	SD1_D[0]	
53	u	u	k	d	d	I	SD1_D[1]	
54	u	u	k	d	d	I	SD1_D[2]	
55	u	u	k	d	d	I	SD1_D[3]	
56	u	u	k	d	d	1)	SD1_CMD	
57	u	u	k	d	d	I	SD1_CLK	
58							UART #1-3	
59	u	u	k	d	d	I	UART1_TXD	
60	u	u	k	d	d	I	UART1_RXD	
61	u	u	k	d	d		UART1_RTS	
62	u	u	k	d	d	I	UART1_CTS	
63	u	u	k	d	d	I	UART2_TXD	
64	u	u	k	d	d	I	UART2_RXD	
65	u	u	k	d	d	I	UART2_RTS	
66	u	u	k	d	d	I	UART2_CTS	
67	u	u	A	d	d	I	UART3_TXD	
68	u	u	A	d	d	I	UART3_RXD	
69	u	u	A	d	d	I	UART3_RTS	
70	u	u	A	d	d	I	UART3_CTS	
71							KEYPAD / CAN	
72	u	u	k	d	d		KP_COL[0]	
73	u	u	k	d	d		KP_COL[1]	

Pin	TX6LVDS	TX6RGB	TXUL	TX8M	TX8P	TXMP		
74	u	u	k	d	d		KEYPAD / CAN	KP_COL[2]
75	u	u	k	d	d			KP_COL[3]
76	u	u	k	d	d	I		KP_COL[4] / TXCAN
77	u	u	k	d	HDMI			KP_ROW[0]
78	u	u	k	d				KP_ROW[1]
79	u	u	k	d				KP_ROW[2]
80	u	u	k	d				KP_ROW[3]
81	d	d	k	d	d	I		KP_ROW[4] / RXCAN
82							SSI #1	
83	u	u	u	d	d	I		SSI1_INT
84	u	u	u	d	d	I		SSI1_RXD
85	u	u	u	d	d	I		SSI1_TXD
86	u	u	u	d	d	I		SSI1_CLK
87	u	u	o	d	d	I		SSI1_FS
88							SSI #2	
89	u	u		d	HDMI			SSI2_INT
90	u	u		d				SSI2_RXD
91	u	u		d				SSI2_TXD
92	u	u		d				SSI2_CLK
93	u	u		d	d			SSI2_FS
94							SD-CARD #2	
95	u	u		d	d			SD2_CD
96	u	u		d	d			SD2_D[0]
97	u	u		d	d			SD2_D[1]
98	u	u		d	d			SD2_D[2]
99	u	u		d	d			SD2_D[3]
100	u	u		d	d			SD2_CMD
101	u	u		d	d			SD2_CLK
102							CAMERA	
103	u	u	k	d	d			CSI_D[0]
104	u	u	k	u	d			CSI_D[1]
105	u	u	k	d	d			CSI_D[2]
106	u	u	k	d	d			CSI_D[3]
107	u	u	k	d	d			CSI_D[4]
108	u	u	k	d	d			CSI_D[5]
109	u	u	k	d	d			CSI_D[6]
110	u	u	k	d	d			CSI_D[7]
111								
112	u	u	k	d	d			CSI_HSYNC
113	u	u	k	d	d			CSI_VSYNC
114	u	u	k	u	d			CSI_PIXCLK
115	d	d	k	d	d			CSI_MCLK
116							RGB - DISPLAY - LVDS	
117	u	LVDS	k	LVDS/MIPI	LVDS/MIPI	I		LCD_D[0] LVDS1_TX2_N
118	u		k			I		LCD_D[1] LVDS1_TX1_N
119	u		k			I		LCD_D[2] LVDS1_TX2_P
120	u		k			I		LCD_D[3] LVDS1_TX1_P
121	u		k			I		LCD_D[4] LVDS1_TX3_N
122	u		k			I		LCD_D[5] LVDS1_TX0_N
123	u		k			I		LCD_D[6] LVDS1_TX3_P
124	u		k			I		LCD_D[7] LVDS1_TX0_P
125	u		k			I		LCD_D[8] LVDS1_CLK_N
126	u		k			I		LCD_D[9] LVDS0_TX3_P
127	u		k			I		LCD_D[10] LVDS1_CLK_P
128	u		k			I		LCD_D[11] LVDS0_TX3_N
129								
130	u	LVDS	k	LVDS/MIPI	LVDS/MIPI	I		LCD_D[12] LVDS0_CLK_P
131	u		k			I		LCD_D[13] LVDS0_TX2_P
132	u		k			I		LCD_D[14] LVDS0_CLK_N
133	u		k			I		LCD_D[15] LVDS0_TX2_N
134	u		k			I		LCD_D[16] LVDS0_TX1_P
135	u		k			I		LCD_D[17] LVDS0_TX0_P
136	u		k			I		LCD_D[18] LVDS0_TX1_N
137	u		k			I		LCD_D[19] LVDS0_TX0_N
138	u	SATA	k		USB3	I		LCD_D[20] SATA/USB3_RXM
139	u		k			I		LCD_D[21] SATA/USB3_TXM
140	u		k			I		LCD_D[22] SATA/USB3_RXP
141	u		k			I		LCD_D[23] SATA/USB3_TXP
142								
143	u	u	k	d	d	I		HSYNC
144	u	u	k	d	d	I		VSYNC
145	u	u	k		d	I		OE_ACD
146	u	u	k	d	d	I		LSCLK

Pin	TX6LVDS	TX6RGB	TXUL	TX8M	TX8P	TXMP		
147							GPIO	
148	u	u	k	d	d	I		
149	Ol	Ol	k	d	d	I		
150	u	u	k	d	d	I		
151	u	u	k	d	d	I		
152	Ol	Ol	k	d	d	I		
153	Ol	Ol	k	u	u	I		HEARTBEAT
154	Ol	Ol		d	d	I		
155	Ol	Ol		U	U	I		
156	Ol	Ol		d	d			
157	Ol	Ol		u	d			
158	Ol	Ol		d	d			
159	Ol	Ol		d	d			
160								
161	u	u		d	d			
162	u	u		d	d			
163	u	u			d			
164	u	u			d			
165	d	d			d		PCI	
166								PCIE_CLK_P
167	PCI	PCI		PCI	PCI	MIPI		PCIE_RXM
168								PCIE_CLK_N
169								PCIE_RXP
170								PCIE_TXM
171								
172								PCIE_TXP
173	Oh	Oh						
174	Oh	Oh						
175	u	u						
176	u	u						
177	Oh	Oh						
178	Oh	Oh			MIPI			
179	Oh	Oh						
180	Oh	Oh						
181	Oh	Oh						
182	Ol	Ol						
183								
184	u	u						
185	u	u						
186	u	u			MIPI			
187	u	u						
188	u	u						
189	u	u						
190	u	u						
191	u	u						
192	u	u						
193	u	u						
194	u	u			MIPI			
195	u	u						
196	u	u						
197	u	u						
198	u	u						
199	u	u						
200								

NOT CONNECTED
NC ON 8M NANO
SPECIAL FUNCTION
GND

Out of Reset Condition

Oh	OUTPUT high
Ol	OUTPUT low
u	PULL-UP
U	Output high during reset, input with PU after reset
d	PULL-DOWN
D	Output low during reset, input with PD after reset
k	KEEPER
a	analog
i	input

1) No standard TX pin assignment